Search Notes

Application/Control No.	Applicant(s)/Patent under Reexamination
10/084,515	BURNLEY, RICHARD P.
Examiner	Art Unit
Leigh Marie Garbowski	2825

	SEARCHED '				
Class	Subclass	Date	Examiner		
716	6,17	2/2/2005	LMG		
	<del>-</del> .				

ERFERENC	ERENCE SEARCHED		
Subclass	Date	Examiner	
		Subclass Date	

SEARCH NOTES (INCLUDING SEARCH STRATEGY)			
	DATE	EXMR	
WEST timing			
core embedded			
pld			
clock-to-output setup			
nold	2/2/2005	LMG	
delay path			
portion memory controller			
fgpa host			
feeding back			
modifications (terms were also varied, i.e. set-up,	2/2/2005	LMG	
modifying, path delay, etc.)	2/2/2000	LIVIG	
<u> </u>			
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